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Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)	Complete if Known	
	Application Number	Unknown 10/777957
	Filing Date	Even Date Herewith 02/13/04
	First Named Inventor	Shirley, Paul
	Group Art Unit	Unknown 1746
	Examiner Name	Unknown CA22116
Sheet 1 of 1	Attorney Docket No: 303.774US2	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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	US-5,860,178	01/19/1999	Nishimura, J , et al.	15	77	03/29/1996
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BSC	JP-2000-33346	02/02/2000	Disco, KK	B08B	003/02	
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
BSC		VAN ZANT, PETER , "Microchip fabrication : a practical guide to semiconductor processing", 4th Edition, New York : McGraw-Hill, Chapter 4 & Chapters 8-9,(2000),87-132 & 193-280	

EXAMINER

*[Signature]*

DATE CONSIDERED

12/16/04

Substitute Disclosure Statement Form (PTO-1449)

\* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached